Pranav Maneriker

pranav.maneriker@dolby.com   pranavmaneriker.github.io   Google Scholar

Summary

PhD in CS (OSU, 2024) building foundational AI for multimodal generation and auditable ML, with publications at CVPR, ICLR, KDD, EMNLP, WWW, and TMLR and 12+ US patents.

Education

The Ohio State University   2018 – 2024
PhD & **MS**, Computer Science and Engineering *(GPA: 4.0/4.0)*; Advisor: Dr. Srinivasan Parthasarathy

IIT Kanpur   2012 – 2016
BTech, Computer Science and Engineering *(GPA: 9.2/10)*; Minor in English Literature

Selected Experience

Dolby Laboratories   Atlanta, GA
Senior Researcher, Experience Delivery   Jun 2024 – Present
Develop and deploy foundational AI models for multimodal content (video, audio, text) enabling Dolby HDR and content generation experiences. Published two papers accepted at CVPR 2026: efficient autoregressive image generation (DPAR) and sparse video understanding and reasoning.

The Ohio State University   Columbus, OH
Graduate Research Associate   Aug 2019 – May 2024
Developed graph-based stylometry (SYSML, EMNLP 2021), online ML fairness auditing (AVOIR, KDD 2023, US Patent), and conformal prediction frameworks (ICLR 2025, US Patent). Co-authored successful NSF ($499,995) and Cisco Research ($199,931) grant proposals.

Johns Hopkins University   Baltimore, MD
Visiting Research Scholar, SCALE 2022   Jun – Aug 2022
Led graph-embedding stylometry work for Author Identification and curated 1B+ post Reddit datasets for scaling-law analysis.

Microsoft Research   Redmond, WA
Research Intern   May – Aug 2020
Scaled 300M-parameter transformer training across multi-node clusters and co-developed URLTran for malicious URL detection (MILCOM 2021, US Patent 12,003,535).

Amazon   Seattle, WA
Applied Scientist Intern   May – Aug 2019
Applied metric and few-shot learning to fraud detection (Buyer Fraud / Transaction Risk Management), improving performance and data efficiency over the deployed system.

Selected Publications

- D Srivastava, A Mehra, **P Maneriker**, D Sanyal, V Raj, V Kamarshi, F Du, J Kimball. **DPAR: Dynamic Patchification for Efficient Autoregressive Visual Generation**, **CVPR** 2026.

- C Xu, Z Ye, S Wu, W Li, Z Wang, Z Xia, L Lu, **P Maneriker**, F Du, M Li, H Liu. **Towards Sparse Video Understanding and Reasoning**, **CVPR** 2026.

- **P Maneriker**, A Vadlamani, A Srinivasan, Y He, A Payani, S Parthasarathy. **Benchmarking Graph Conformal Prediction**, **TMLR** 2025.

- **P Maneriker**, C Burley, S Parthasarathy. **Online Fairness Auditing through Iterative Refinement**, **KDD** 2023.

Full list: Google Scholar

Patents, Awards & Service

- **Patents**: 12+ US patents, including conformal fairness evaluation and auditing (Provisional US Patent App. 63/772,024, 2025), systems and methods for measuring and auditing fairness (US Patent App. 18/419,130, 2024), and phishing URL detection using transformers (US Patent 12,003,535, 2024).

- **Awards & Service**: WWW 2020 Best Paper Award; OSU CSE Graduate Research Award and Graduate Poster Award (2023); Aditya Birla Group Scholarship (2012–2016). Reviewer for WWW, AAAI, EMNLP, ACL, KDD, SDM, ICLR, IEEE TPAMI, JMLR, IJCV, and Springer DAMI; Student Volunteer at SIGIR 2019 and KDD 2023; co-authored NSF #2133650 ($499,995) and Cisco Research ($199,931) grants.